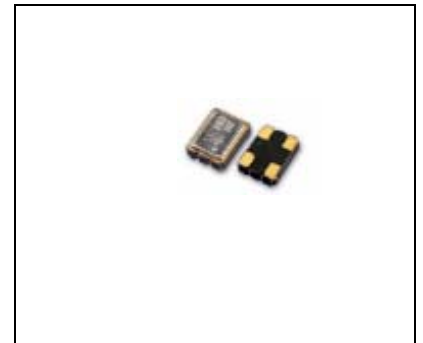


Type PX smd clock oscillator (1.0 ~ 200.0)MHz

- # (3.2 x 2.5)mm, height 1.3mm, smd
- # Tight symmetry (45~55%) standard
- # RoHS Compliant standard



Electrical Specification

Case Style	type PX: (3.2 x 2.5)mm, height 1.3 mm			
Frequency range	(1~133)MHz: 1.8V, (1~166)MHz:2.5V, (1~200)MHz:3.3V			
Stability*	±(20~100)ppm, temperature range dependent			
Supply voltage $V_{DD} \pm 10\%$		+1.8V	+2.5V	+3.3V
	1MHz ≤ Fo < 30MHz	6mA	8mA	10mA
	30MHz ≤ Fo < 75MHz	8mA	10mA	15mA
	75MHz ≤ Fo < 133MHz	12mA	15mA	20mA
	133MHz ≤ Fo < 166MHz	-	15mA	22mA
	166MHz ≤ Fo ≤ 200MHz	-	-	25mA
Rise and fall time max.**	1MHz ≤ Fo < 10MHz	10nSec	8nSec	6nSec
	10MHz ≤ Fo	6nSec	5nSec	5nSec
Operating temperature	(-10 + 60)°C ~ (-40 + 85)°C			
Storage temperature	(-55 + 125)°C			
Symmetry	45~55% as standard			

*Inclusive of calibration @ 25°C, operating temperature range, input voltage variation, load variation, aging, shock, and vibration.

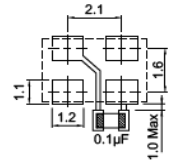
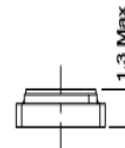
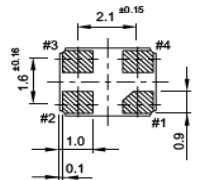
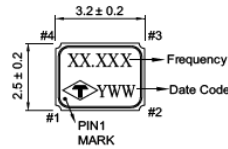
**Transition times are measured between 10% and 90% of VDD, with an output load of 15pF.

Ordering Information

TFC PART NUMBER PX 13.0M E T D C J
 (1) (2) (3) (4) (5) (6) (7)

- 1) **Type:** PX = smd clock oscillator type PX
- 2) **Frequency:** 13.0M = 13.0MHz,
frequency range from (1.0 ~200.0)MHz
- 3) **Supply voltage:** K: +1.8Vd.c. J: 2.5Vd.c. E: +3.3Vd.c.
- 4) **Tri state:** F: fixed frequency, T: tri-state function on pin#1
- 5) **Frequency stability :** C: ±20ppm, D: ±25.0ppm,
G: ±50ppm, H: ±100ppm
- 6) **Temperature range :** I: (-10 to +60)°C,
C: (-20 to +70)°C, L: (-40 to +85)°C
- 7) **Output logic and symmetry:** J: CMOS 15pF, (45~55)%

PX dimensions(mm)



Recommended soldering pattern

- #1 tri-state
- #2 GND
- #3 output
- #4 V_{DD}