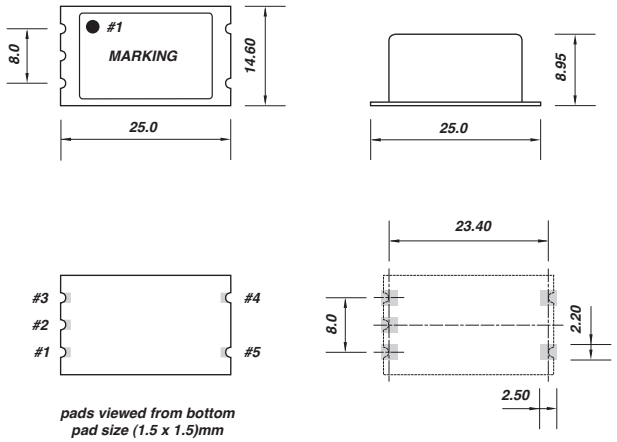
Optimised performance 10.00MHz low voltage smd DIL OCXO

- **Temperature tolerance: ±0.02ppm(-20 +70)°C**
- **Phase noise: -160dBc/Hz, f_0 +1KHz**
- **Low profile smd package**
- **SC cut crystal**
- **Supply (3.3, 5, 9 or 12)Vd.c.**
- **Quiescent current: 140mA max. at +25°C**
- **RoHS compliant**

Generic specification:

frequency:	10.000MHz
output:	CMOS 15pF, 45% ~ 55% <5ns max. rise and fall
stability:	
against temperature	±0.02ppm(-20 +70)°C
against V_{cc} change	±0.002ppm max., V_{cc} ±5%
against load change	±0.002ppm max., load ±10%
ageing short term	±0.0002ppm max. per day after 30 days continuous operation
ageing long term	±0.05ppm max. per year after 30 days continuous operation
voltage trim V_t	±1ppm typical, 1.5Vd.c. ±1.5Vd.c. linearity ±5%
trim input impedance	100KΩ min.
power supplies:	
supply voltage V_{cc}	(3.3, 5, 9 or 12)Vd.c.
voltage reference	+3Vd.c.
start up current	220mA, 12Vd.c. supply 370mA, 9Vd.c. supply 540mA, 5Vd.c. supply 820mA, 3.3Vd.c. supply
quiescent current	100mA, 12Vd.c. supply 140mA, 9Vd.c. supply 220mA, 5Vd.c. supply 350mA, 3.3Vd.c. supply
warm up time	2 minutes max. to within 0.1ppm of nominal
insulation resistance	500MegΩ min., 100Vd.c.
phase noise:	-130dBc/Hz, f_o+10Hz -150dBc/Hz, f_o+100Hz -160dBc/Hz, f_o+1kHz
temperature:	
operating range	(-20 +70)°C
storage range	(-40 +125)°C
marking:	part number, frequency, date code, serial number

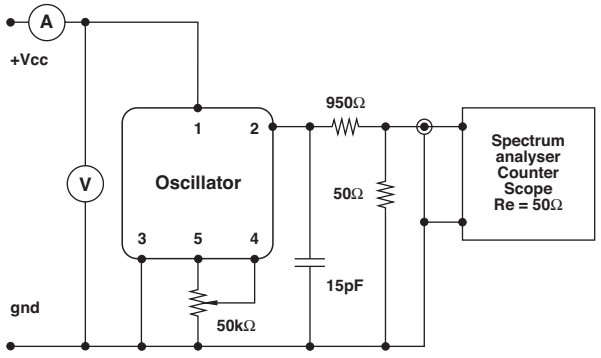
Dimensions(mm):



Pin connections:

# 1	$+V_{cc}$
# 2	output
# 3	ground/case
# 4	tune
# 5	V_{ref}

Test circuit, CMOS load:



test circuit includes a 20:1 step down into a matched 50Ω load

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