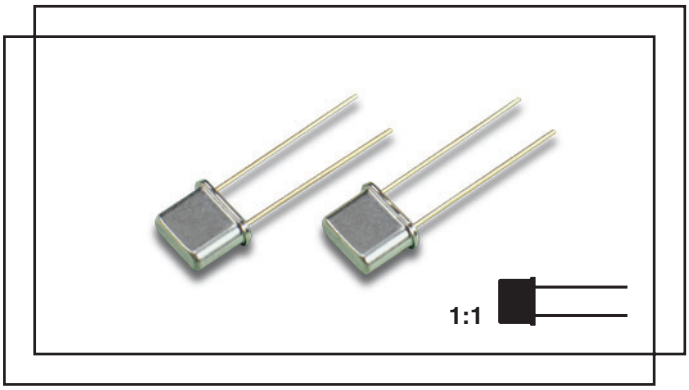
LT4 subminiature leaded crystal (25.0 ~ 200)MHz

- # custom frequencies
- # AT cut fundamental to 40.0MHz
- # high shock and vibration



Electrical specification

Case style	LT4
Frequency range	(25.0 ~ 200)MHz, fundamental to 40.0MHz
Adjustment tolerance	from ±10ppm at +25°C, frequency dependent
Temperature tolerance	from ±3ppm, frequency and temperature range dependent
Operating temperature	(-10 +60)°C ~ (-40 +85)°C
Storage temperature	(-40 +85)°C
Load	customer specified
Shunt capacitance C_0	5.0pF max.
Drive level	100µW max.
Q factor	100,000 typical
Ageing	±2ppm nominal first year
Insulation resistance	500Meg. ohm min. at 100Vd.c.

Ordering information

The LT4 and crystal may be specified within its available frequency range together with load capacitance, adjustment tolerance, temperature tolerance and temperature range with each parameter coded as follows ....

Example .... LT4 crystal, 25.00MHz, load 20pF, ±20ppm at +25°C, ±20ppm(-10 +60)°C

TFC PART NUMBER .... LT4 25.00M H C C I

'LT4' .... crystal holder
'25.00M' .... frequency: 25.00M = 25.00MHz, frequency range from (25.0 ~ 200)MHz
'H' .... load capacitance: H = 20pF
'C' .... adjustment tolerance at +25°C: C = ±20ppm
'C' .... temperature tolerance: C = ±20ppm
'I' .... temperature range: I = (-10 +60)°C

***Load capacitance** .... C: 10pF, D: 12pF, E: 15pF, G: 18pF, H: 20pF, I: 30pF, J: 32pF, S: series*
***Adjustment tolerance** .... A: ±5ppm, B: ±10ppm, C: ±20ppm, E: ±30ppm, G: ±50ppm, H: ±100ppm, P: ±15ppm*
***Temperature tolerance** .... A: ±5ppm, B: ±10ppm, C: ±20ppm, E: ±30ppm, G: ±50ppm, H: ±100ppm, P: ±15ppm*
***Temperature range** .... B: (0 +55)°C, I: (-10 +60)°C, C: (-20 +70)°C, L: (-40 +85)°C*

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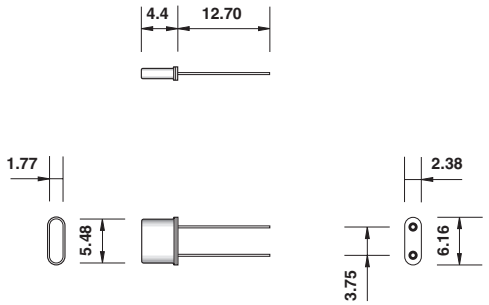
ISO9001: 2000
RS13124
ISO14001: 1996
EMS53965

LT4 subminiature leaded crystal

LT4 dimensions(mm)



Lead diameter 0.30 Footprint

ESR - equivalent series resistance

frequency range(MHz)	cut/mode	esr(Ω)
(25.0 ~ 30.0)	AT1	<80
(30.0 ~ 40.0)	AT1	<60
(40.0 ~ 90.0)	AT3	<70
(90.0 ~ 150.0)	AT3	<60
(100.0 ~ 200.0)	AT5	<120

Environmental test conditions

Thermal shock	MIL STD 202F method 107, Test condition A
Seal	MIL STD 202F method 112, Test condition C
Vibration	MIL STD 202F method 201, Test condition C
Solderability	MIL STD 202F method 208, procedure 4.0
Acceleration	MIL STD 202F method 107, Test condition B

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