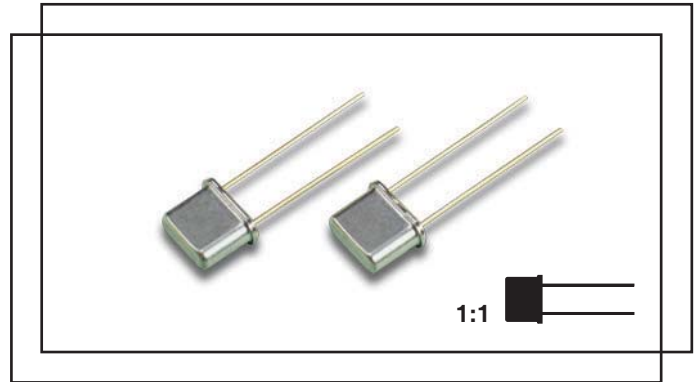


LT4 subminiature leaded crystal (25.0 ~ 200)MHz

- # custom frequencies
- # AT cut fundamental to 40.0MHz
- # high shock and vibration



Electrical specification

Case style
Frequency range
Adjustment tolerance
Temperature tolerance
Operating temperature
Storage temperature
Load
Shunt capacitance C_0
Drive level
Q factor
Ageing
Insulation resistance
LT4

(25.0 ~ 200)MHz, fundamental to 40.0MHz
 from ± 10 ppm at $+25^\circ\text{C}$, frequency dependent
 from ± 3 ppm, frequency and temperature range dependent
 $(-10 +60)^\circ\text{C} \sim (-40 +85)^\circ\text{C}$
 $(-40 +85)^\circ\text{C}$
 customer specified
 5.0pF max.
 100 μW max.
 100,000 typical
 ± 2 ppm nominal first year
 500Meg. ohm min. at 100Vd.c.

Ordering information

The LT4 and crystal may be specified within its available frequency range together with load capacitance, adjustment tolerance, temperature tolerance and temperature range with each parameter coded as follows

Example LT4 crystal, 25.00MHz, load 20pF, ± 20 ppm at $+25^\circ\text{C}$, ± 20 ppm $(-10 +60)^\circ\text{C}$

TFC PART NUMBER LT4 25.00M H C C I

'LT4' crystal holder

'25.00M' frequency: 25.00M = 25.00MHz, frequency range from (25.0 ~ 200)MHz

'H' load capacitance: H = 20pF

'C' adjustment tolerance at $+25^\circ\text{C}$: C = ± 20 ppm

'C' temperature tolerance: C = ± 20 ppm

'I' temperature range: I = $(-10 +60)^\circ\text{C}$

Load capacitance C: 10pF, D: 12pF, E: 15pF, G: 18pF, H: 20pF, I: 30pF, J: 32pF, S: series

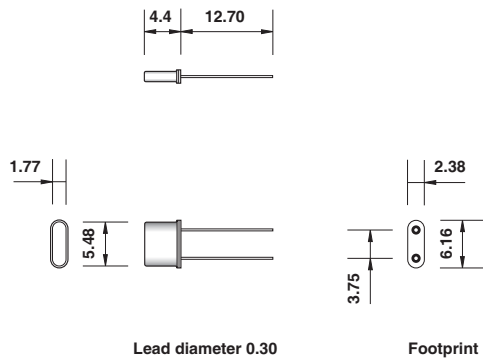
Adjustment tolerance A: ± 5 ppm, B: ± 10 ppm, C: ± 20 ppm, E: ± 30 ppm, G: ± 50 ppm, H: ± 100 ppm, P: ± 15 ppm

Temperature tolerance A: ± 5 ppm, B: ± 10 ppm, C: ± 20 ppm, E: ± 30 ppm, G: ± 50 ppm, H: ± 100 ppm, P: ± 15 ppm

Temperature range B: $(0 +55)^\circ\text{C}$, I: $(-10 +60)^\circ\text{C}$, C: $(-20 +70)^\circ\text{C}$, L: $(-40 +85)^\circ\text{C}$

LT4 subminiature leaded crystal

LT4 dimensions(mm)



ESR - equivalent series resistance

frequency range(MHz)	cut/mode	esr(Ω)
(25.0 ~ 30.0)	AT1	<80
(30.0 ~ 40.0)	AT1	<60
(40.0 ~ 90.0)	AT3	<70
(90.0 ~ 150.0)	AT3	<60
(100.0 ~ 200.0)	AT5	<120

Environmental test conditions (on request)

Thermal shock	MIL STD 202F method 107, Test condition A
Seal	MIL STD 202F method 112, Test condition C
Vibration	MIL STD 202F method 201, Test condition C
Solderability	MIL STD 202F method 208, procedure 4.0
Acceleration	MIL STD 202F method 107, Test condition B