

Type BJ clock oscillator 4 pin(14 pin DIL layout) (1.0 ~ 70)MHz

- # ±10ppm tolerance
- # standard and custom frequencies
- # +3.3Vd.c., +5.0Vd.c. supply

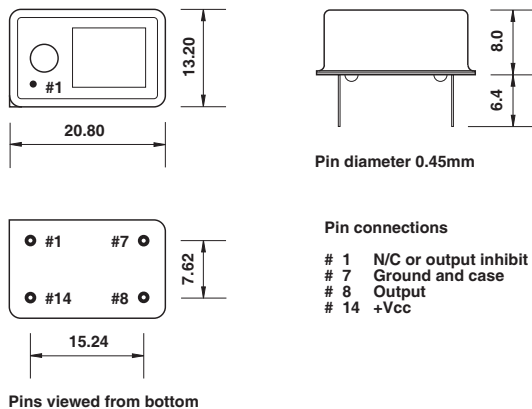


Electrical specification

Case style	4 pin(14 pin DIL layout): (20.8 x 13.2)mm, height 8.0mm		
Frequency range	(1.0 ~ 70)MHz		
Stability *	±(10 ~ 25)ppm, temperature range dependent		
Supply voltage V_{cc}		+3.3Vd.c.	+5.0Vd.c.
Supply current max.	(1.0 ~ 20)MHz	10mA	15mA
	(20 ~ 50)MHz	20mA	40mA
	(50 ~ 70)MHz	40mA	50mA
Rise and fall time max. **	(1.0 ~ 20)MHz	10ns	8ns
	(20 ~ 70)MHz	6ns	5ns
Operating temperature	(-20 +70)°C ~ (-40 +85)°C		
Storage temperature	(-55 +125)°C		
Output	TTL, CMOS		
Symmetry	(45 ~ 55)%, (40 ~ 60)%		
Tri-state	fixed frequency or tri-state		
Ageing	±5ppm first year max.		

* inclusive of calibration tolerance at +25°C, temperature tolerance, supply voltage variation, load variation, first year ageing, shock and vibration.
 ** measured, with an output load of 15pF, between (10 ~ 90)% V_{CC}

Dimensions(mm), through hole



Dimensions(mm), gull wing

