TA265 - 8.5

- **±1.0ppm, excellent phase noise, low ageing**
- **A miniature 14 pin DIL resistance weld package, 8.5mm tall, manufactured to standard and custom specifications over the frequency range of 10MHz to 250MHz**
- **Precision crystals provide outstanding long term ageing from ±4.6ppm over 10 years**

Standard options:

frequency range:		(10 ~ 250)MHz		
accuracy codes:	(A)	(B)	(C)	
temperature tolerance	±1.0ppm	±1.5ppm	±2.0ppm	
temperature range	(-10 +60)°C	(-20 +70)°C	(-35 +70)°C	
output codes:	(S)		(L)	
output	sine wave, 0dBm into 50Ω harmonics -30dBc max.		CMOS 15pF, 45% ~ 55% <2ns max. rise and fall	
supply voltage codes:	(V1)	(V2)	(V3)	
supply voltage	+3.3Vd.c.	+5.0Vd.c.	+12.0Vd.c.	

Generic specification:

stability:			
against supply voltage change	±0.02ppm max. for V_{cc} ±5%		
against load change	±0.02ppm max. for load ±10%		
ageing short term	±0.005ppm max. per day after 30 days continuous operation		
ageing long term	±1.5ppm max. first year		
voltage trim V_t	±10ppm min. typical, linearity ±5%		
trim input impedance	100KΩ min.		
power supplies:			
supply voltage V_{cc}	+3.3Vd.c.	+5.0Vd.c.	+12.0Vd.c.
supply current	50mA max. frequency dependent		
insulation resistance	500MegΩ min., 100Vd.c.		
phase noise:			
single sideband, 1Hz bandwidth	-80dBc/Hz, f_o+10Hz -100dBc/Hz, f_o+100Hz -125dBc/Hz, f_o+1kHz		
temperature:			
operating range	(-10 +60)°C	(-20 +70)°C	(-35 +70)°C
storage range	(-40 +125)°C	(-40 +125)°C	(-40 +125)°C

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Environmental conditions:

mechanical shock:	*MIL standard 202F, method 213, condition J*
thermal shock:	*MIL standard 202F, method 107, condition A*
vibration:	*MIL standard 202F, method 204, condition B*
solderability:	*5 seconds max. at +230°C, 3 seconds max. at +350°C*

Marking:

part number and frequency on high temperature metalised polyester label

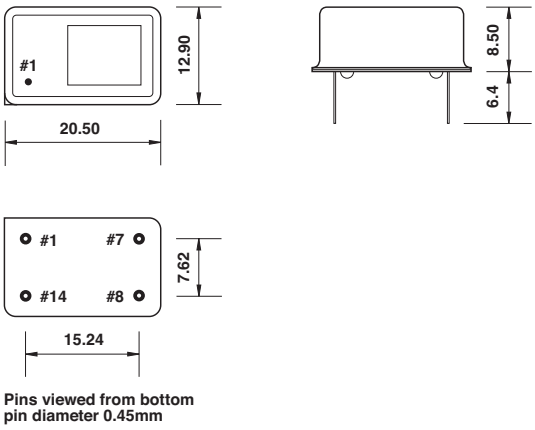
Ordering code:

standard specification:	***TA265-8.5 A S V2 - 18.432M***
TA265-8.5	***= series generic code***
A	*temp. tol. and temp. range code:* ***A = ±1.0ppm(-10 +60)°C***
S	*output code:* ***S = sine wave output, 0dBm into 50Ω***
V2	*supply voltage code:* ***V2 = +5Vd.c. supply***
18.432M	*output frequency:* ***18.432M = 18.432MHz***

Custom specification:

part number issued with custom specification and drawing

Dimensions(mm):

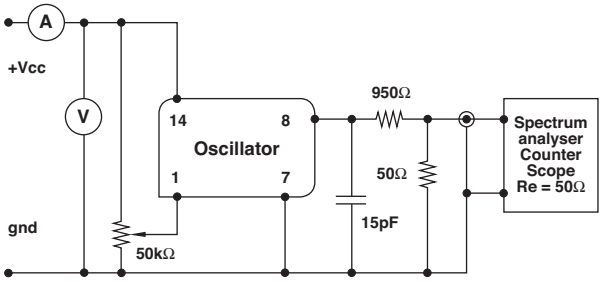


Pins viewed from bottom
pin diameter 0.45mm

Pin connections:

#1	*trim*
#7	*ground/case*
#8	*output*
#14	*+V_{cc}*

Test circuit, CMOS load:



test circuit includes a 20:1 step down into a matched 50Ω load

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